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V. S.
9/29/08

REPLY UNDER 37 CFR 1.116
EXPEDITED PROCEDURE
TECHNOLOGY CENTER 2800

Docket No.: 524322000200
(PATENT)

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Patent Application of:
Dennis CIPLICKAS et al.

Application No.: 10/541,076

Confirmation No.: 4078

Filed: June 29, 2005

Art Unit: 2825

For: DESIGNING AN INTEGRATED CIRCUIT TO
IMPROVE YIELD USING A VARIANT
DESIGN ELEMENT (AS AMENDED)

Examiner: V. Siek

AMENDMENT UNDER 37 C.F.R. 1.116

MS AF
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Dear Sir:

This is in response to the final Office Action dated July 17, 2008, (Part of Paper No./Mail Date 20080716), for which a response is due on October 17, 2008. Additionally, this response is filed within two months of the mailing date of the final Office Action and therefore qualifies for expedited review. Reconsideration and allowance of the pending claims, as amended, in light of the remarks presented herein are respectfully requested.